

EAST - [Untitled1:1]

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 L1: (35) (bus near3 ar
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 DBs: USPAT ☒ Plurals
 Default operator: OR ☒ Highlight all hit terms initially

(bus near3 arbitration) and (grant\$3 same
(concurrent\$2 or simultaneous\$2) same latenc\$3)

☒ BRS form ☒ IS&R form ☐ Image ☐ Text ☐ HTML

	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current X
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6950892 B2	20050927	13	Method and system for managing distributed ar	710/120	710/119; 710/240;
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6807593 B1	20041019	9	Enhanced bus architecture for posted	710/110	710/112; 710/113;
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6779036 B1	20040817	24	Method and apparatus for achieving correct o	709/232	370/352; 370/396;
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6725307 B1	20040420	29	Method and system for controlling data transf	710/110	710/105; 710/107;
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6721836 B2	20040413	11	Bus system having an address/control bus and	710/240	370/912
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6684279 B1	20040127	28	Method, apparatus, and computer program produc	710/241	710/105; 710/107;
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6671761 B2	20031230	20	Bus system	710/244	710/107; 710/240;
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6606676 B1	20030812	31	Method and apparatus to distribute interrupts t	710/262	710/260; 710/264;
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6542949 B1	20030401	28	Method and apparatus for increased performan	710/113	
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6535941 B1	20030318	35	Method and apparatus for avoiding data bus a	710/241	710/113; 710/116
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6529990	20030304	37	Method and apparatus to	710/310	711/146